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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Douglas D. Coolbaugh, et al.

Examiner: Cuong Q. Nguyen

Serial No: 09/516,615

Art Unit: 2811

Filed: March 1, 2000

Docket: BUR919990190US1 (13020)

For: METHOD OF FABRICATING A  
POLYSILICON CAPACITOR UTILIZING  
FET AND BIPOLAR BASE POLYSILICON  
LAYERS

Dated: July 7, 2003

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

RESPONSE

Sir:

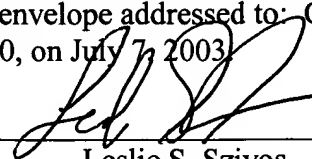
In response to the Office Action dated March 7, 2003, applicants submit the following amendments and remarks for entry of record in the above-identified patent application.

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I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on July 7, 2003.

Dated: July 7, 2003

  
Leslie S. Szivos